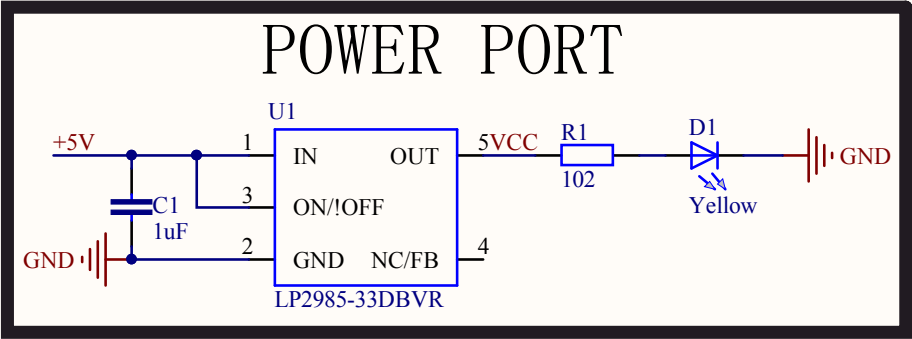


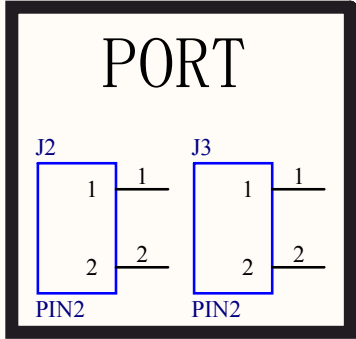
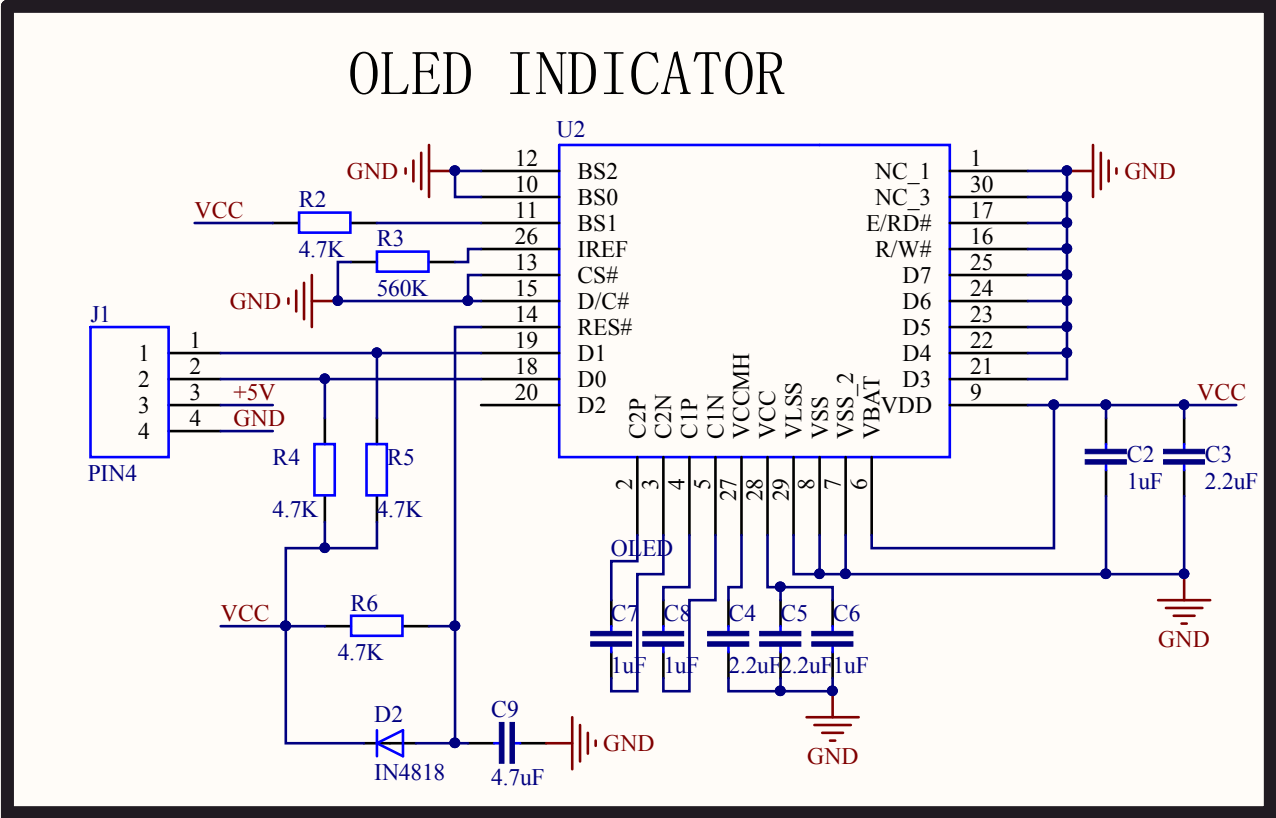
POWER PORT

The diagram illustrates a power port circuit. It features a +5V input line and a GND line. A 1uF capacitor (C1) is connected between the +5V line and GND. The input of the LP2985-33DBVR voltage regulator (U1) is connected to the +5V line (pin 1). The output of the regulator (pin 4) is connected to a 5VCC line. A 102 resistor (R1) is connected between the 5VCC line and the anode of a yellow LED (D1). The cathode of the LED is connected to GND. The regulator's ON/OFF pin (pin 3) is connected to the +5V line, and its GND/NC pin (pin 2) is connected to GND.



PORT

The diagram illustrates two ports, J2 and J3, each represented by a blue rectangle. Port J2 has two pins: pin 1 is connected to a line labeled 1, and pin 2 is connected to a line labeled 2. The label PIN2 is positioned below the rectangle. Port J3 has two pins: pin 1 is connected to a line labeled 1, and pin 2 is connected to a line labeled 2. The label PIN2 is also positioned below this rectangle.

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